



## Aleti Ravichandra

Faculty Id	:	
Date of Birth	:	02 July , 1986
Designation	:	Assistant Professor.
Years of Experience	:	Teaching : 9 years
		Industry : Nil
Email Id	:	Jntuh.ravi@gmail.com
Phone Number	:	9290705049
Faculty Web Page	:	
Employment Status	:	Full Time - Ratified by JNTUH
Areas of Specialization	:	<ul style="list-style-type: none"><li>• VLSI Design.</li><li>• Embedded Systems.</li><li>• Microprocessors and Microcontrollers.</li><li>• Digital signal Processing.</li><li>• IC Applications.</li><li>• Cellular Mobile Communication.</li></ul>
UG Degree	:	ELECTRONICS AND COMMUNICATION ENGINEERING,2008, CECC,CHIRALA, PRAKASHEM, A.P
PG Degree	:	VLSI & EMBEDDED SYSTEMS,2012, CECC,CHIRALA, PRAKASHEM, A.P
Ph.D	:	Pursing, VBSPU, Jaunpur.
Ph.D Thesis topic	:	VLSI Design.
UG Subjects Taught	:	VLSI Design, Embedded Systems, Microprocessors and Microcontrollers, Digital signal Processing, IC Applications, Cellular Mobile Communication, Switching Theory and Logic Design, Embedded And Real Time Operating Systems.
PG subjects Taught	:	Digital Signal Processors And Architectures, Algorithms For VLSI Design Automation, VLSI Technology and Design.

Projects handled  
(Consultancy)

: Nil

#### Papers Published:

##### International Journals

☞ Published an international Journal of "**Implementation of Secured Hash Algorithm -1 on Verilog**" in International Journal of Electronics, Communication & Instrumentation Engineering Research and Development(IJECIERD)-ISSN 2249-684X Vol.2, Issue 3 (Spl.) Sep 2012.

<http://www.tjprc.org/view-archives.php?>

☞ Published an international Journal of "**An Advance Technology In Automotive Felony By Victimization Pca Algorithmic Program**" in International Journal of Engineering & Science Research(IJESR) ISSN: 2277 - 2685 Volume 3, Issue 10, October 2013.

<http://www.ijesr.org/Archivesearchonline.aspx>

☞ Published an international Journal of "**VLSI Implementation of Impulse Noise Suppression in Images**" in International Journal of Application or Innovation in Engineering & Management (IJAIEM) ISSN: 2319 – 4847 Volume 2, Issue 10, October 2013.

<http://www.ijaiem.org/V2I10.html>

☞ Pub Published an international Journal of "**Design and Verification of a MAC controller with inclusion of Promiscuous mode, based on AXI bus**" in International Journal of Advanced Research Foundation (IJARF) ISSN: 2394 -3394 Volume 1, Issue 2, November 2014.

<http://ijarf.com/november/>

☞ Published an international Journal of "**Comparative Evaluation of SE and TIC Models for Low Switching Activity for Serial Link Communication**" in International Journal of Advanced Research Foundation(IJARF) ISSN: 2394 -3394 Volume 2, Issue 7, July 2015.

<http://ijarf.com/july/>

**Papers reviewed as Reviewer**      None

**Membership/Participations:**      None

**Workshops attended:**

**1. Mobile Apps In Indian Languages** – National Workshop on Mobile Apps In Indian Languages on 04<sup>th</sup> July 2015 is organized by C-DAC JNTUH Campus.

<http://cdachyd.in/events-conferences/Workshop-on-Mobile-Security//>

**2. Digital Design Using Verilog-HDL** – One day workshop on The Art of Digital Design Using VERILOG – HDL on Nov 14<sup>th</sup> , 2014 conducted by G.Narayanamma Institute of Technology & Science Autonomous – JNTUH, Accredited by NAAC, Hyderabad.

**Achievement**      None

**Books Published:**

None

**Research Projects Undertaken:**

None