



C. Deepika

Faculty Id	:	2226-171128-132319
Date of Birth	:	27 January, 1991
Designation	:	Assistant Professor
Years of Experience	:	1.5
Email Id	:	deepika.c@kgr.ac.in
Phone Number	:	9121316390
Faculty Web Page	:	
Employment Status	:	Full Time - Ratified by JNTUH
Areas of Specialization	:	<ul style="list-style-type: none">• Electronic Circuits and Devices• Low Power VLSI• Communication Systems• Digital Electronics
UG Degree	:	ECE, 2012, JACSICE, Tamilnadu
PG Degree	:	Applied Electronics, 2014, Chandy College of Engineering, Tamilnadu .
UG Subjects Taught	:	Electronic Devices, Electronic Circuits, Embedded and Real time Systems, Wireless Communication Network
PG subjects Taught	:	Analog Integrated Circuit Design
Projects handled	:	1. Study and experimental investigation on RF power measurement

Papers Published:

International Journals

- **C.Deepika**, MA.Sohana Parveen, Poonam Swami “*DUAL NODE PULSE DOMINO TECHNIQUE FOR BUFFER CIRCUIT IN LOW POWER MEMORY ARRAYS*” IJCRT | Volume 6, Issue 2 April 2018, pp 572-578.
http://www.ijcrt.org/viewfull.php?p_id=IJCRT1812941
- **C. Deepika**, Poonam Swami, MA. Suhana Praveen, C. Deepika “*SURVEY ON MIMO OFDM WITH ADVANCED INDEX MODULATION*” 2018 IJCRT Volume 6, Issue 2 April 2018 ISSN: 2320-2882.
(http://www.ijcrt.org/viewfull.php?p_id=IJCRT1812851)

Workshops attended:

- ☞ Attended one day workshop on “Networking Essentials” conducted by Talent Zone, Tamilnadu.
- ☞ Attended one day workshop on “Introduction of Robotics and its Applications” conducted by NTT, Tamilnadu.
- ☞ Attended 2 days national workshop on “VLSI Design and Implementation using XILINX” conducted by Dr.Sivanthi Aditanar College of Engineering, Tiruchendur, Tamilnadu.

Books Published:

None

Research Projects Undertaken:

None