



P. Spandana

Faculty Id	:	
Date of Birth	:	04 January, 1991
Designation	:	Assistant Professor.
Years of Experience	:	Teaching : 3 years
		Industry : 1 Year
Email Id	:	spandana@kgr.ac.in
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Employment Status	:	Full Time - Ratified by JNTUH
Areas of Specialization	:	<ul style="list-style-type: none">• Embedded system design• Image processing• Communications
UG Degree	:	Electronics and communication ENGINEERING,2012, RRSCET,JNTUH, Patancheru, Telangana
PG Degree	:	Embedded system design, 2015, Swathi college of science and technology, OU,Hyderabad, Telangana.
UG Subjects Taught	:	Electronic devices and circuits, Basic electrical and electronics engineering, Electronic circuit analysis, Analog communications, Switching theory and logic design

Papers Published:


-  **P.SPANDANA** , "CBIR Using Color, GLCM & Discrete Wavelet Transform" National conference conducted in KG Reddy College of Engineering &Technology in December-2016
<http://ijiet.com/wp-content/uploads/2016/12/1123.pdf>

-  **P. SPANDANA and U.ALAVELU** , " A Relative Execution Investigation of OFDM using MATLAB Recreation with M-PSK and M-QAM Mapping" National conference conducted in KG Reddy College of Engineering &Technology in December-2017
<http://www.ijcrt.org/papers/IJCRTNCES041.pdf>

-  **P.SPANDANA and U.ALAVELU** " An application of combinatorial analysis HGLC for image restoration" National conference conducted in KG Reddy College of Engineering &Technology in December-2017.
<http://www.ijcrt.org/papers/IJCRTNCES044.pdf>

-  **P.SPANDANA and A.DEEPIKA** "PROCESSING AND ANALYSIS OF DIGITALIMAGES AND CHECKING THE QUALITY OF DATA CAPTURED" in Journal of Emerging technologies and innovative research June-2018.
<http://www.jetir.org/papers/JETIR1806097.pdf>

Membership/Participations:

-  Participated in 2 day Faculty development program on "Teaching and Learning methods" at IIT Kandi, Sangareddy, Telangana.

Books Published:

None

Research Projects Undertaken:

None