



Dr. Manish Jain

Faculty Id	:	4440-160312-085119
Date of Birth	:	18 August, 1980
Designation	:	Associate Professor
Years of Experience	:	Teaching : 16 years
	:	Research : 3 years
Email Id	:	dean-a @kgr.ac.in
Phone Number	:	+91 9602644810
UG Degree	:	Electronics & Communication Engineering , 2001, RGPV, Bhopal, M.P.
PG Degree	:	Microelectronics & VLSI Design 2008, SGSITS, Indore, M.P.
Ph. D	:	Electronics & Communication, 2015, PAHER University
Ph. D Thesis topic	:	New Algorithm implementation for performance implantation of ADC using histogram test Technique
Employment Status	:	Full Time - Ratified by JNTUH.
Areas of Specialization	:	<ul style="list-style-type: none">• VLSI Design and Testing• ADC Design & Testing• Digital Design, FPGA based Design• Digital Signal Processing• Microprocessor & Microcontroller
UG Subjects Taught	:	Digital Electronics, Circuit Theory, Electronics Devices & Circuits, Microprocessor & Interfaces, Signals & Systems, Digital Signal Processing, VLSI Design, IC Technology

PG subjects Taught

: VLSI Test & Testability , RF Communication, DSP Application

Papers Published:

International Journals/Conference

-  Paper entitled **“Digital image watermarking using Hybrid DWT-FFT Technique with Different Attacks”**, Published in IEEE sponsored International Conference on Communication and Signal processing at Adhiparasakthi Engg. College, Melmaruvathur, Tamilnadu, India.
-  Paper entitled **“FPGA based Network on chip implementation of scalable router”** has been presented at ‘Int’l Conference on VLSI Design and Embedded Systems (ICVLSI’08) held at Velammal Engg.Collage, Chennai (T.N.) from 14-16 Feb 2008.
-  Paper entitled **“Design & optimization of Noise & power Consumption of a Two Stage CMOS Op-Amp”** has been presented at ‘National Conference on Emerging Trends on Information Technology’ held at SGSITS, Indore (M.P.) from 18-20 Dec. 2007.
-  Paper entitled **“OP-AMP Design for 12 bit ADC with fast settling time and higher bandwidth ”** has been presented at ‘National Conference on Challenges of Power Sector in 21st Century’ held at JEC, Jabalpur (M.P.) from 13-14 Nov. 2007.
-  Manish Jain and Reema Jain, **“Digital image watermarking”**, Presented in National Conference on Recent Trends & Issues in Engineering and Technology (NCRTIET-2014) at Modi Institute of Technology, Kota, on Oct. 18, 2014.
-  Manish Jain and R. S. Gamad, **“Design and performance verification of current conveyor based pipeline A/D converter using 180 nm technology”**, Published in European Scientific Journal, Vol. 8, No.27, pp 118-131, November 2013.
-  Manish Jain and R. S. Gamad, **“Estimation of ENOB of A/D converter using histogram test technique”**, Published in International Journal of Electronics and Communication Engineering & Technology (IJECET), Vol.4, issue 4.
-  Manish Jain and Reema Jain, **“Digital Image Watermarking using 3-Level DWT and FFT via Image Compression”**, Published in International Journal of Computer Applications (0975 – 8887) Volume 124 – No.16, August 2015.
-  Manish Jain, Sonali Singh, **“ Efficient Hartley Discrete Transform using Vedic & Kogge-Stone Adder”** Published in International Journal of Innovative Research in Science , Engineering & Technology” Vol. 4, issue 11,(Nov.16), pp 2874-2881, ISSN(Online) : 2319-8753.
-  Manish Jain, Sonali Singh, **“Survey of Discrete Hartley Transform using different types of Methods”** Published in International Journal of Innovative Research in Computer &

Communication Engineering" Vol. 6, issue 2,(Feb 17), pp 2874-2881, ISSN(Online) : 20538-20543.



Manish Jain & Mukta Thankachan, "**FPGA based Design and Simulation of Extended Golay codec with delay optimization for high speed application**"

Published in International Journal of Engineering Research" Vol. 4, issue 6, pp 170-177, ISSN (online): 2321-7758, Nov.-Dec, 2016.



Manish Jain & Mukta Thankachan, "**Design and Simulation of Extended Golay code on FPGA for long distance applications- A Review**" Published in International Journal of Computer Science & Engineering Technology (IJCSET)" Vol.7, No. 11, issue 6, pp 445-448, ISSN (online): 2349-4689.



Manish Jain and R. S. Gamad, "**A simple & time effective method for measurement of SNR & SINAD in time domain ADC testing**", published in International Journal of Electronics & Communication Engineering (IJRECE), Vol. 2, issue 4, August 2014.

M.Tech.Ph.D. Thesis Guided:-

M.Tech. - 07

Ph.D - 01 , 03(p)

Membership/Editorial board member:

- ☞ Life Time Membership of IAENG(Hong Kong),
- ☞ Member of Institute of Engineers (IEI)
- ☞ Editorial board Member of Conference world.

Workshops/Conferences/STTP attended:

- ☞ Attended workshop on **“Digital Signal Processor, MSP 430 and their application”** at Rajasthan Technical University, Kota (Rajasthan), India during January 28-30, 2011.
- ☞ Attended three days **“Entrepreneurship Awareness Camp sponsored by Deptt. of Science and technology (DST), Govt. of India” at Modi Institute of Technology, Kota (Raj.) during Dec. 27-29, 2012.**
- ☞ Attended two days workshop on **“Auto Botz- AVR”conducted by Technophilia Systems in association with Robotics & Computer Applications Institute of USA** held at Modi Institute of Technology, Kota (Rajasthan), India during February-March 28-01, 2014.
- ☞ Attended fifteen days **“Faculty Development Programme in Entrepreneurship”** sponsored by Deptt. of Science and technology (DST), Govt. of India” at Modi Institute of Technology, Kota (Raj.) during April 15-28, 2014.
- ☞ Attended one week short term course on **“Power Electronics Drives & MATLAB Applications (PEDMA-2013)”** organized by Deptt. of Electrical Engg., Rajasthan Technical University, Kota (Rajasthan), During March, 18-22 2013.
- ☞ Attended AICTE sponsored winter school on **“ADSP & Application”** at Raj.Inst.of Tech. & Mgt. Jaipur (Raj.) during Jan.-Feb. 27- 06, 2009.
- ☞ Attended the National Workshop on **“Recent Trends in Microelectronics & VLSI Design”** at **S.G.S.I.T.S. Indore** (M.P.) during May 24-26, 2007.
- ☞ Attended the National Workshop on **“Mobile Communication”** at **Govt. Engg. College Ujjain** (M.P.) during March 16-18, 2007.

Achievement

- ☞ Certificate of Appreciation by Technofilia for faculty coordinator in AutoBotz-AVR workshop part of Indo-US Robo league 2014.
- ☞ Financial Assistance by DST Govt. of Rajasthan to the Student project under my guidance in the session Feb. 2012-13 & 2013-14.

Books Published:

"Digital Signal Processing" CBC publication Jaipur Sept.2009. **Authors**-Manish Jain & Gaurav Vijay.

Research Projects Undertaken:

None