



MA.Sohana Parveen

Faculty Id	:	74150402-153627
Date of Birth	:	05August , 1989
Designation	:	Assistant Professor
Years of Experience	:	Teaching : 6 years
		Industry : -
Email Id	:	sohana@kgr.ac.in
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Employment Status	:	Full Time - Ratified by JNTUH
Areas of Specialization	:	<ul style="list-style-type: none">• VLSI System design• Physical design• Signal processing
UG Degree	:	ELECTRONICS & COMMUNICATION ENGINEERING,2010, BITS, Khammam, Telangana
PG Degree	:	VLSI SYSTEM DESIGN, 2012, BITS, Khammam, telangana.
UG Subjects Taught	:	Signals & systems, digital design through verilog, switching theory and logic design, digital logic design, electronic measurement and instrumentation, control system engineering, digital signal processing, analog communications, antenna and wave propagation, microwave engineering, radar systems
PG subjects Taught	:	CAD for vlsi circuits and cmos analog IC design.
UG Projects handled	:	15

Papers Published:

International Journals

☞ **MA.sohana parveen**, kotha swapnika and M.basha "*DESIGN OF HIGH SPEED FLOATING POINT UNIT USING REVERSIBLE LOGIC*" IJMETMR, volume 3, issue 9 September 2016, ISSN: 2348-4845.

(<http://www.ijmetmr.com/olseptember2016/KothaSwapnika-MASuhanaParveen-MBasha-62.pdf>).

☞ **MA. Sohana Praveen**, Poonam Swami and C. Deepika "SURVEY ON MIMO OFDM WITH ADVANCED INDEX MODULATION" 2018 IJCRT Volume 6, Issue 2 April 2018 ISSN: 2320-2882.

(http://www.ijcrt.org/viewfull.php?p_id=IJCRT1812851)

☞ **MA.Sohana Parveen**, C.Deepika and Poonam Swami "dual node pulse domino technique for buffer circuit in low power memory arrays" IJCRT, Volume 6 , issue 2 April 2018 , pp 572-578 .

(https://waset.ijcrt.org/viewfull.php?p_id=IJCRT1812941)

Books Published:

None

Research Projects Undertaken:

None