



M.N.NARSAIAH

Faculty Id	:	7495-150409-120025
Date of Birth	:	04 April, 1980.
Designation	:	Associate Professor & HOD.
Years of Experience	:	Teaching : 14 years
	:	Industry : Nil
Email Id	:	hod.ece@kgr.ac.in
Phone Number	:	7660000964,9640378185
UG Degree	:	Electronics and Communication Engineering (ECE),2004, VJIT, Hyderabad , Telangana.
PG Degree	:	VLSI System Design, 2011, JBREC College, Hyderabad, Telangana.
(Ph. D)	:	Signal Processing, JNTU, Hyderabad.
Employment Status	:	Full Time - Ratified by JNTUH.
Areas of Specialization	:	<ul style="list-style-type: none">• Signals and systems• Analog Communication• Digital communication• Signal/Image processing• Telecommunication Switching System• Cellular and Mobile Communications• Wireless Communications and Networks
UG Subjects Taught	:	Network Analysis, Signals and systems, Probability Theory and Stochastic Random Process, Switching Theory and Logic Design, Linear System Analysis, Principles of Communications, Analog Communication, Control

		System Digital communication, Digital Signal processing , Digital Image processing ,Telecommunication Switching System, Cellular and Mobile Communications, Wireless Communications and Networks, TV Engineering.
PG subjects Taught	:	VLSI design, Digital System Design.

PAPERS PUBLISHED:

INTERNATIONAL JOURNALS

- ☞ M.N.Narasaiah, Dr. S Vathsal, Dr.D Venkat Reddy, “A Survey on image fusion requirements, techniques, evaluation metrics and its applications”, IJET, june 2018.
- ☞ M.N.Narasaiah,”web surveillance system using raspberry pi”, IJERT, Volume 4, issue-03, march 2015.
- ☞ M.N.Narasaiah, Dr. S Vathsal, Dr.D Venkat Reddy, “Air craft navigation sensors/systems and the significance of multisensor data fusion”, IJCRT, Dec 2017.
- ☞ M.N.Narasaiah, Dr. S Vathsal, Dr.D Venkat Reddy, “MULTISENSOR DATA FUSION FOR AIRCRAFT NAVIGATION: OVERVIEW AND METHODOLOGY”, IJIET, Dec 2016.

WORKSHOPS ATTENDED:

- ☞ One week refresher programmer for faculty on electrical circuits and network analysis organized by department of electrical and electronics engineering department from 26/09/2005 to 01/10/2005.
- ☞ Two day national level workshop on “FPGA Synthesis and Simulations using XLINX tools” organized by Department of ECE, Vidya Vikas Institute of Technology from 18/08/2009 to 19/09/2009.
- ☞ Four day Staff Development Program on “DSP processor Architecture and Programming” organized by Department of ECE, in association with ISTE chapter &IIPC of VNR VJIT During 19/12/2011 to 23/12/2011.
- ☞ Two days Workshop on “Embedded system Technology” conducted by ECE Dept, KG Reddy College of Engineering and Technology. 23rd and 24th Sept. 2013
- ☞ One day complimented seminar on “Matlab and simulink for engineering education” conducted by MathWorks India on 11-8-2014
- ☞ Two days Workshop on “Design and implementation of student centered learning” conducted by footsteps and international institute for developing engineering academics (IIDEA),at KG Reddy College of Engineering and Technology. 5and 6 jan 2016.
- ☞ 10 days Workshop on “Intelligent signal processing and control system design using bio inspired algorithms” conducted by E & ICT NIT Warangal during 22nd – 31st May 2017.
- ☞ One week FDP conducted by CEED, KG Reddy College of Engineering and Technology. During 4th to 9th Dec 2017
- ☞ Two week FDP on “Teaching and learning and technical knowledge and skills enhancement program” conducted by CEED at KG Reddy College of Engineering and Technology. During 14th to 26th May 2018
- ☞ Three days workshop on “To train the trainers for implementation of induction program” conducted by JNTUH being o organized under TEQIP-III. During 17th to 19th May 2018

ACHIEVEMENT:

- ☞ Excellence in teaching award for securing 100% passes in “Wireless Communications and Networks”.
- ☞ Excellence in teaching award for securing 100% passes in “Cellular and Mobile Communications”.
- ☞ Excellence in teaching award for securing 100% passes in “Telecommunications Switching Systems”.
- ☞ Felicitation by Rotaract District organization on 5-sep-2014.