



PENKI RAMESH

Faculty Id	:	
Date of Birth	:	10June , 1985
Designation	:	Assistant Professor.
Years of Experience	:	Teaching : 6 years
		Industry : 0 years
Email Id	:	ece6@kgr.ac.in
Phone Number	:	8919048329
Employment Status	:	Full Time - Ratified by JNTUH
Areas of Specialization	:	VLSI SYSTEM DESIGN
UG Degree	:	ELECTRONICS & COMMUNICATIONS ENGINEERING,ELURU, A.P.
PG Degree	:	Aurora's engineering college, Bhongir, Nalgonda, Telangana.
Ph.D	:	Not yet registered
Ph.D Thesis topic	:	---
UG Subjects Taught	:	VLSI Design, Pulse and Digital Circuits, Electronic Devices and Circuits, Signal and Systems, Analog Communications, Radar Systems, Digital Logic Design, Digital signal processing. PTSP, Electronics circuits, Control systems.
PG subjects Taught	:	Low power VLSI Design, Embedded real time operating system

Papers Published:

International Journals

- Published a paper on “Embedding of Iris Data to Hand Vein Images Using Watermarking Technology to Improve Template Protection in Biometric Recognition”, IJMETMR, ISSN no: 2348-4845.

<http://www.ijmetmr.com/olnovember2016/GarikaSreekanth-PenkiRamesh-32.pdf>

Workshops attended:

- Two weeks National level Workshop on “Advanced VLSI Technology” Department of ECE at Geethanjali College of Engineering & Technology in Hyderabad.
- One week National level Workshop on “SIGNAL ANALYSIS AND TRANSFORM TECHNIQUES” organized by the E & ICT Academy, NIT Warangal, at Vignan’s Lara institute of technology & science.
- Two days “National Conference on Engineering, Science, Technology in Industrial Applications and Significance of Free Open Source Software” (NCESTFOSS-2017) at KG Reddy College of Engineering & Technology in Hyderabad
- One week FDP on “EFFECTIVE TEACHING & LEARNING” Between 14/5/18 to 18/5/18 at KG Reddy College of Engineering & Technology in Hyderabad.

Achievement

- I have qualified CBSE-UGC NET-NOVEMBER 2017.
- I have qualified GATE-2006 & 2007.