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(57) Abstract :
A TURBO-ENCODER MODULE FOR IN-VEHICLE SYSTEM A Turbo encoder that can be incorporated into the in-vehicle system (IVS) modem's and the turbo encoder is designed using FPGA technologies to improved chip size and processing speed. The input signal to a turbo encoder consists of the MSD data plus the CRC parity bits. MSD data blocks are 1148 bits in length. The module's output is binary data that has been MSD-encoded. When utilizing a turbo coding technique with an excitation bit rate of 1:3, the output length is 3456 bits. Fig 2

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